

University of Engineering and Technology Taxila

Sub Campus Chakwal

End Semester Examination (Spring 2013)
2K11 Electronic Engineering (4th Semester)

Course Title: **Microprocessors and Microcontrollers**

Total Marks: 40

Name _____

Time Allowed: 45 mins

Roll # _____

Note:-Erased and overwritten answers will be considered as wrong.

Q1 (a).Only encircle the appropriate answer. (15 Marks)

- 1) Which of the following buses is the fastest?
a) ISA b) EISA c) PCI Express d) VESA
- 2) The resident local bus is often called the _____.
a) Front side bus b) main bus c) PCI bus d) control bus
- 3) **8086** is a 16-bit microprocessor with a _____ bit data bus.
a) 6 b) 16 c) 32 d) 8
- 4) 8284A is what?
a) DMA Controller b) Buffer c) Clock Generator d) Timer
- 5) Each Bus Cycle consists of _____ clock periods.
a) 4 b) 3 c) 2 d) 1
- 6) When a memory chip is **not enabled** (i.e. it is not selected) then its data lines?
a) Give a 0
b) Give a 1
c) Are at high impedance state
d) Do not change
- 7) A 2764 EPROM will have _____ memory
a) 8K X 8 b) 4K X 8 c) 32K X 8 d) 16K X 8
- 8) After hardware reset, the 8088 picks up code form _____ location (cold start).
a) FFFFF b) FFFF0 c) 0000F d) 11111
- 9) The two instructions that transfer the data between an I/O device and processor are?
a) Bound/un-Bound b) RD/WR c) INPUT/OUTPUT d) IN/OUT

7) What are three modes of serial communication?

8) What is the role of first/last flip flop in 8237 DMA controller?

9) What is noise immunity? What is Fanout?

10) What is minimum and maximum mode of microprocessor?